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jc678 U.S. PTO
10/30/01PATENT APPLICATION
Attorney's Do. No. 4591-220

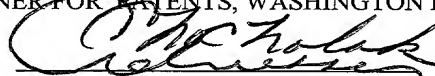
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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10/30/01

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I HEREBY CERTIFY THAT THIS PAPER AND ENCLOSURES AND/OR FEE ARE BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED ABOVE AND IS ADDRESSED TO: BOX PATENT APPLICATION, ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON D.C. 20231.

ADRIENNE CHOCHOLAK
(SENDER'S PRINTED NAME)
(SIGNATURE)Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor Jae-Yong JEONG and Sung-Soo LEE:
For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE WITH A
FAIL BIT DETECTING SCHEME AND METHOD FOR COUNTING
THE NUMBER OF FAIL BITS

Applicant requests FIG. 16 to be published with the application.

Enclosures:

- ☒ Specification (pages 1-14); claims (pages 15-19); abstract (page 20)
- ☒ SIXTEEN sheet(s) of FORMAL drawings
- ☒ Declaration or Combined Declaration and Power of Attorney
- ☒ Newly executed (original or copy)
- ☒ Assignment with cover sheet
- Assignee Name and Address: Samsung Electronics Co., Ltd.
Suwon-city, Kyungki-do, Korea
- ☒ Certified copy of priority document
- ☒ Return Postcard

CLAIMS AS FILED

For	Number Filed	Number Extra	Rate	Basic Fee \$740
Total Claims	30-20	10	x \$ 18 =	180
Independent Claims	4-3	1	x \$ 84 =	84
TOTAL FILING FEE				\$1004



Information Disclosure Statement with Form PTO 1449
PTO Form 2038 authorizing credit card payment for the above-listed fees is enclosed.
Any deficiency or overpayment should be charged or credited to deposit account
number 13-1703. A duplicate copy of this sheet is enclosed.



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PATENT TRADEMARK OFFICE

Respectfully submitted,

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